

10/733,671

In the Claims

Claim 1 (Previously Cancelled)

Claim 2 (Previously Cancelled)

Claim 3 (Previously Cancelled)

Claim 4 (Previously Cancelled)

Claim 5 (Previously Cancelled)

Claim 6 (Previously Cancelled)

Claim 7 (Previously Cancelled)

Claim 8 (Previously Cancelled)

Claim 9 (Previously Cancelled)

Claim 10 (Previously Cancelled)

Claim 11 (Previously Cancelled)

Claim 12 (Previously Cancelled)

Claim 13 (Previously Cancelled)

Claim 14 (Previously Cancelled)

Claim 15 (Previously Cancelled)

Claim 16 (Previously Cancelled)

Claim 17 (Currently Amended) A method of forming a memory cell, comprising:

forming rails of semiconductor material on a substrate;

doping a first portion of said rails;

10/733,671

forming a dielectric on said first portion of at least every other one of said rails;
forming a plate electrode on said first portion of adjacent pairs of said rails;
forming an FET in horizontally along a second portion of said rails adjacent to said first portion which contain the FET source and drain, said FET having a gate electrode disposed on all exposed sides of a part of said second portion of said rails.

Claim 18 (Currently Amended) The method of claim 17, wherein said substrate comprises a SOI substrate having a buried insulator layer, and wherein said ~~plurality of~~ rails are disposed on and contact said buried insulator layer.

Claim 19 (Original) The method of claim 17, wherein dielectric is formed on said first portions of all of said rails on which a memory cell is to be formed.

Claim 20 (Original) The method of claim 19, wherein said rails have a height of at least approximately 0.15 microns.

10/733,671

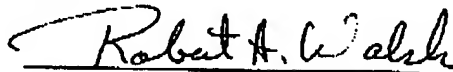
Conclusion

Based on the foregoing, it is respectfully submitted that all the claims are properly listed in compliance with 37 CFR § 1.121 .

The Examiner is urged to call the undersigned at the number listed below if, in the Examiner's opinion, such a phone conference would aid in furthering the prosecution of this application.

Respectfully submitted,
For: Jeffrey S. Brown, et al.

BY:



Robert A. Walsh, Reg. # 26,516

Telephone No.: (802) 769-9521

Fax No.: (802) 769-8938

EMAIL: walshra@us.ibm.com

International Business Machines Corporation
Intellectual Property Law - Mail 972E
1000 River Road
Essex Junction, VT 05452